

**IN THE CLAIMS**

Please cancel claims 2 and 6 without prejudice or disclaimer as to their subject matter, amend claims 1, 7, 8 and 17 and newly add claims 21 through 24 by this amendment as follows:

1           1. (Currently Amended) A plasma display panel, comprising:  
2           a front substrate and a rear substrate opposing one another with a predetermined gap  
3           therebetween;  
4           a plurality of display electrodes formed on the front substrate;  
5           a dielectric layer formed on the front substrate covering the display electrodes;  
6           a plurality of first barrier ribs and a plurality of second barrier ribs formed on the rear  
7           substrate essentially perpendicular to each other forming an array of discharge cells, each  
8           discharge cell being completely surrounded by said first and said second barrier ribs;  
9           a plurality of phosphor layers formed in the discharge cells; and  
10          a plurality of electrically conductive address electrodes being formed orthogonal to  
11          the display electrodes in the discharge cells, said address electrodes being parallel to said  
12          first barrier ribs, the address electrodes being coated with a dielectric material, wherein a  
13          phosphor layer is further coated on an outer circumference of the dielectric material coating  
14          the address electrodes.

1           Claim 2 (Canceled)

1           3. (Original) The plasma display panel of claim 1, further comprising fixing grooves  
2           formed in edges of the rear substrate at areas corresponding to terminal areas of each of the  
3           address electrodes, the fixing grooves securing the terminal ends of the address electrodes.

1           4. (Original) The plasma display panel of claim 3, wherein the terminal areas of the  
2           address electrodes positioned in the fixing grooves are further secured by an adhesive  
3           member.

1           5. (Original) The plasma display panel of claim 1, wherein a height  $t_2$  of the second  
2           barrier ribs is less than a height  $t_1$  of the first barrier ribs.

1           Claim 6 (Canceled)

1           7. (Currently Amended) The plasma display panel of claim 1, wherein the conductive  
2           wires forming the address electrodes are circular in cross section.

1           8. (Currently Amended) The plasma display panel of claim 1, wherein the conductive  
2           wires forming the address electrodes are polygonal in cross section.

1           9. (Original) The plasma display panel of claim 1, wherein the discharge cells defined

1 by the first barrier ribs and the second barrier ribs have a polygonal shape when viewed from  
2 a direction of the front substrate.

1 10. (Original) The plasma display panel of claim 1, wherein the discharge cells  
2 defined by the first barrier ribs and the second barrier ribs, have a circular shape when  
3 viewed from a direction of the front substrate.

1 11. (Original) The plasma display panel of claim 1, wherein the discharge cells  
2 defined by the first barrier ribs and the second barrier ribs, are rectangular and staggered to  
3 discharge cells on an opposite side of a first barrier rib.

1 12. (Original) A plasma display panel, comprising:  
2 a front substrate and a rear substrate opposing one another with a predetermined gap  
3 therebetween;  
4 a plurality of display electrodes formed on the front substrate;  
5 a dielectric layer formed on the front substrate covering the display electrodes;  
6 a plurality of barrier ribs formed on the rear substrate and comprising a plurality of  
7 first barrier rib members formed in a direction orthogonal to the display electrodes, and a  
8 plurality of second barrier rib members formed in a direction parallel to the display  
9 electrodes, the first barrier rib members intersecting the second barrier rib members, the  
10 plurality of barrier ribs forming an array of discharge cells, each discharge cell being

11 bounded by a pair of first barrier rib members and a pair of second barrier rib members;  
12 a phosphor layer being formed in respective discharge cells; and  
13 address electrodes comprising conductive wires and coated with a dielectric material,  
14 the address electrodes being mounted on the second barrier rib members, the address  
15 electrodes being orthogonal to the display electrodes.

1 13. (Original) The plasma display panel of claim 12, wherein grooves are formed in  
2 distal ends of the second barrier rib members into which the address electrodes are inserted.

1 14. (Original) The plasma display panel of claim 12, wherein a height  $t_2$  of the  
2 second barrier rib members are less than a height  $t_1$  of the first barrier rib members.

1 15. (Original) The plasma display panel of claim 12, further comprising fixing  
2 grooves formed in edges of the rear substrate at areas corresponding to terminal areas of each  
3 of the address electrodes, the fixing grooves securing the terminal areas of the address  
4 electrodes.

1 16. (Original) The plasma display panel of claim 15, wherein the terminal areas of  
2 the address electrodes positioned in the fixing grooves are further secured by an adhesive  
3 member.

1           17. (Currently Amended) The plasma display panel of claim 12, wherein a phosphor  
2   layer ~~is coated on~~ surrounds an outer circumference of the dielectric material ~~coating that,~~  
3   in turn, surrounds the address electrodes.

1           18. (Original) The plasma display panel of claim 12, wherein the conductive wires  
2   forming the address electrodes are circular in cross section.

1           19. (Original) The plasma display panel of claim 12, wherein the conductive wires  
2   forming the address electrodes are polygonal in cross section.

1           20. (Original) The plasma display panel of claim 1, wherein the address electrodes  
2   are realized through electrically conductive wires.

1           21. (New) The plasma display panel of claim 1, each of the plurality of address  
2   electrodes being completely surrounded by the dielectric material and the dielectric material  
3   being completely surrounded by the phosphor layer.

1           22. (New) The plasma display panel of claim 1, each of the plurality of address  
2   electrodes being mounted on the second barrier ribs.

1           23. (New) The plasma display panel of claim 12, each of the address electrodes being

2 orthogonal to the display electrodes.

1 24. (New) The plasma display panel of claim 12, each of the address electrodes  
2 running orthogonal to the second barrier rib members.